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Sub-Kelvin cooling for two kilopixel bolometer arrays in the PIPER receiver F

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ABSTRACT

The Primordial Inflation Polarization Explorer (PIPER) is a balloon-borne telescope mission to search for inflationary gravitational waves from the early universe. PIPER employs two 32×40 arrays of superconducting transition-edge sensors, which operate at 100 mK. An open bucket Dewar of liquid helium maintains the receiver and telescope optics at 1.7 K. We describe the thermal design of the receiver and sub-Kelvin cooling with a continuous adiabatic demagnetization refrigerator (CADR). The CADR operates between 70 and 130 mK and provides $\approx 10 \mu\text{W}$ cooling power at 100 mK, nearly five times the loading of the two detector assemblies. We describe electronics and software to robustly control the CADR, overall CADR performance in flightlike integrated receiver testing, and practical considerations for implementation in the balloon float environment.

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I. INTRODUCTION

Large cryogenic detector arrays provide revolutionary sensitivity in modern instruments for surveys across the

electromagnetic spectrum, from centimeter wavelengths to calorimetry of energetic particles. Transition edge sensor (TES) and kinetic inductance detector (KID) detection approaches in these instruments require sub-Kelvin operating temperatures to reach

required sensitivity. For temperatures below approximately 300 mK, adiabatic demagnetization refrigeration (ADR) and dilution refrigeration are the leading approaches and have been used in space applications, with recent examples in Hitomi¹ and Planck.² The cooling mechanism of the ADR is solid-state and has no moving parts, making it mechanically robust and long-lived for space applications. A continuous ADR (CADR) is a variant that further provides isothermal cooling power that remains uninterrupted through the ADR cycle.

Here, we describe the receiver and sub-Kelvin cooling for the Primordial Inflation Polarization Explorer (PIPER).^{3–6} PIPER's mission is to map the polarization of the cosmic microwave background (CMB) in a search for inflationary B-modes on large angular scales. During day-long conventional balloon flights, it will map the CMB and Milky Way polarized dust emission in four frequency channels from 200 to 600 GHz.

PIPER consists of twin co-pointed telescopes cooled to 1.5–1.7 K within a large liquid helium (LHe) bucket Dewar. The LHe is pumped by the atmosphere at the balloon float altitude of 30–37 km. A variable-delay polarization modulator (VPM) on each telescope modulates between linear and circular polarization through the translation of a mirror behind a wire grid.⁵ Three advantages of this cryogenic modulation scheme are that (1) the wire grid is large enough to be the first optical element at the 29 cm entrance pupil (with 40 cm open aperture),³ (2) small linear (rather than rotary) translations allow 4 Hz modulation, and (3) a cryogenic wire grid will not emit in-band. Silicon reimaging optics⁷ feed two 32×40 TES focal planes.⁸ For sub-Kelvin cooling, PIPER uses a CADR to maintain the detector arrays at 70–130 mK.

The PIPER receiver presents unique challenges which are not present in traditional cryogenic designs. Section II reviews the architecture and constraints of the receiver. Section III describes sub-Kelvin cooling in the receiver, followed by a description of the CADR drive electronics, harnesses, and control software in Sec. IV. Section V outlines CADR control and operational considerations, and Sec. VI reviews the cryogenic performance of the CADR in combination with the TES arrays.

This publication is the first description of CADR performance in conjunction with large TES arrays for continuum radiation. The previous literature⁹ describes preliminary CADR operation for TES microcalorimeters. The MAX/MAXIMA/MAXIPOL^{10–13} and MSAM2^{14,15} balloon missions for the CMB employed single-shot ADRs based on ferric ammonium alum salts, which derive from prototypes for the Space Infrared Telescope Facility.^{16,17} Sounding rockets for X-ray calorimetry^{18,19} have also employed single-shot ADRs.

Results on CADR performance are from flightlike tests of the integrated receiver at NASA Goddard Space Flight Center in June 2018. The 2018 Ft. Sumner, New Mexico flight campaign was terminated before a PIPER launch opportunity, and the integrated instrument is expected to fly in Fall 2019 from Ft. Sumner. Data from an October 2017 engineering flight from Ft. Sumner demonstrate the cryogenic environment at float altitude, operation of the flight electronics, and recovery after landing. That flight fielded one engineering detector array and demonstrated the CADR in single-shot mode with flight electronics. A cold-open contact that appeared after

shipping (Sec. V) limited demonstration of continuous operation in flight.

II. RECEIVER OVERVIEW

A. Overview and operational environment

A large, open bucket Dewar (Fig. 1) houses the twin telescopes and ascends to 36 km target altitude on a 34×10^6 cubic foot (heavy variant) balloon. PIPER launches with ≈ 3000 L of LHe, and the Dewar is 1.5 m inner diameter by 2 m deep. A hatch covers two optical exit ports on the ground and opens at float altitude.

Using heritage from the ARCADE mission²⁰ (1) superfluid pumps cool the optics to ≈ 2 K, and (2) high gas flow keeps the optics clean without the need for an emissive window. The helium loss rate at float was measured to be 105 L/h in the 2017 engineering flight, corresponding to $7 \text{ m}^3/\text{s}$ gas evolution. Boil-off during ascent consumes approximately 30% of the initial helium fill, so an initial fill of 3000 L results in a 20 h hold at balloon float altitude. This duration is sufficient for the approximately 12 h science period from sunset to sunrise in a conventional campaign. The passive loss rate while waiting for the flight is 220 L/day. Figure 2 shows the receiver and optics temperatures during the ascent of the engineering flight.

All structural elements in the Dewar are stainless steel, which provides low heat conduction through the walls of the Dewar and frame of the telescope, as well as homogeneous contraction through a shared coefficient of thermal expansion (CTE). Only the mirrors are aluminum, and mount flexures maintain alignment while taking up the differential CTE with the frame. The receiver houses two 32×40 TES focal planes that are cooled by the CADR to a nominal bath temperature of 100 mK.

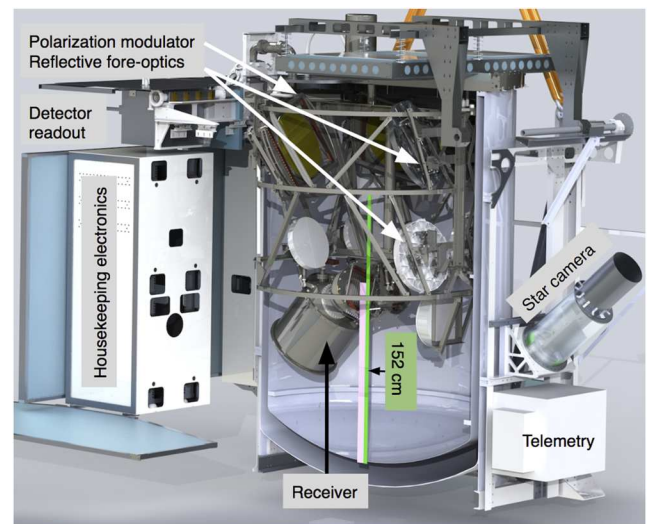


FIG. 1. PIPER is a balloon-borne polarimeter designed to search for inflationary gravitational waves in observations from 200 to 600 GHz. The payload is 2360 kg (dry) and is built around a Dewar filled with 3000 L LHe. A variable-delay polarization modulator (VPM) precedes the off-axis telescope and silicon reimaging optics that feed two 32×40 TES arrays. The twin sides of the telescope measure Stokes Q and U, respectively. The pink and green lines indicate LHe level gauges, which are used to show scale.

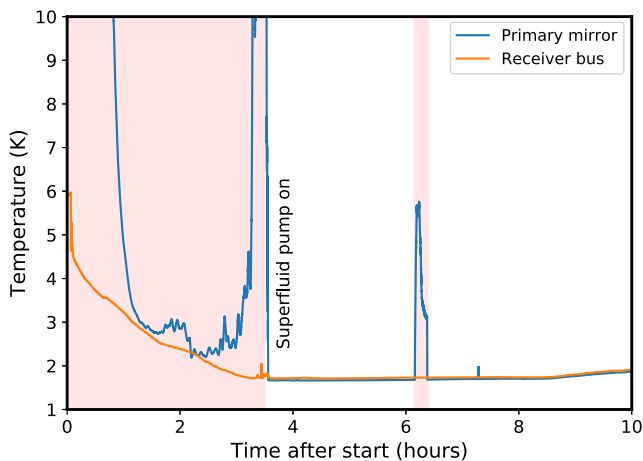


FIG. 2. Temperature data from the PIPER 2017 engineering flight. During the ascent, rapid helium gas evolution carries heat away from the receiver and telescope. LHe becomes a superfluid at altitudes above approximately 24 km, which allows operation of the superfluid pumps. Red regions indicate times when the superfluid pump was not operating. Ascent to float altitude requires approximately 3 h. After the ascent, the superfluid bath stabilizes, and the receiver and optics remain in a weak vacuum above the superfluid. Without superfluid pumps, the telescope temperature rises quickly from parasitic loading, with the primary approaching 35 K. (The optics remain sufficiently cold to contribute negligibly to the photon background if the pumps fail.) Both optics and receiver remain at 1.7 K when the pumps are on. Approximately 6 h into the engineering flight, the pumps were turned off to assess their impact.

B. Receiver architecture

The receiver (Fig. 3) acts as a superfluid-tight enclosure within the parent Dewar. Superfluid films in the receiver can spoil both the TES detector and CADR performance by shorting out thermal

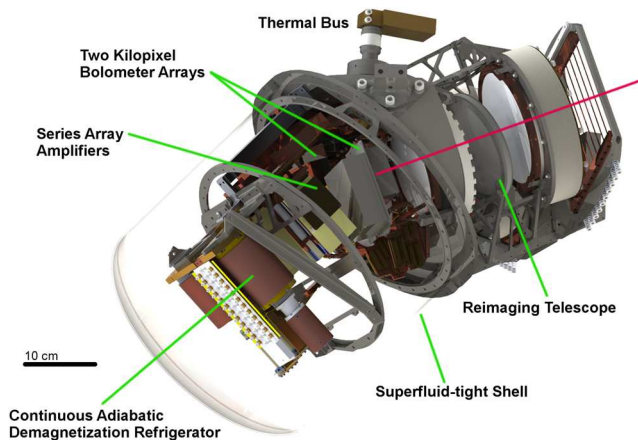


FIG. 3. A section of the PIPER receiver. The red arrow indicates the path of light through the analyzer grid, cold stop, first silicon lens, filter stack, window, second silicon lens, band-defining filter, and to the 32×40 TES array. The receiver serves the twin telescopes, so contains two sets of reimaging optics and both detector arrays.

suspensions. Considerable effort has gone into verifying the superfluid tightness of all interfaces.

Except for a final lens, the reimaging optics reside immediately outside the enclosure's window and comprise (from the sky to detectors) a polarization analyzer grid, the cold stop, the first silicon reimaging lens, and a radiation filter stack. The windows are fused quartz, AR-coated with Teflon,²¹ and sealed against the receiver using indium. These have $\approx 15\%$ measured loss in the 200 GHz band but are 1.7 K so do not contribute to detector loading.

Directly inside the superfluid enclosure, following the window, a silicon lens couples light to the two 32×40 TES focal planes. (The same receiver enclosure houses the detector arrays for both telescopes.) Gold-plated copper boxes house the arrays. A final band-defining filter closes the package, and epoxy loaded with stainless steel and silica^{22,23} blackens the interior of the package. Two groups of 32 superconducting quantum interference device (SQUID) series array amplifiers mount to the main thermal bus, behind the lens and focal plane assemblies. Finally, the CADR is cantilevered from the lid by a drum frame, and connected by the main thermal bus to the helium bath.

The receiver design has two practical constraints (1) the diameter of the receiver must be smaller than the 48.58 cm inner diameter of the integration and test Dewar (Sec. II D), requiring a very compact assembly, (2) the receiver lid (Fig. 4) must act as the sole thermal, vacuum, optical, mechanical, and electrical interface in both the test and flight cryostats. The lid must also serve as an optical alignment bench, and manage the plane change from the receiver

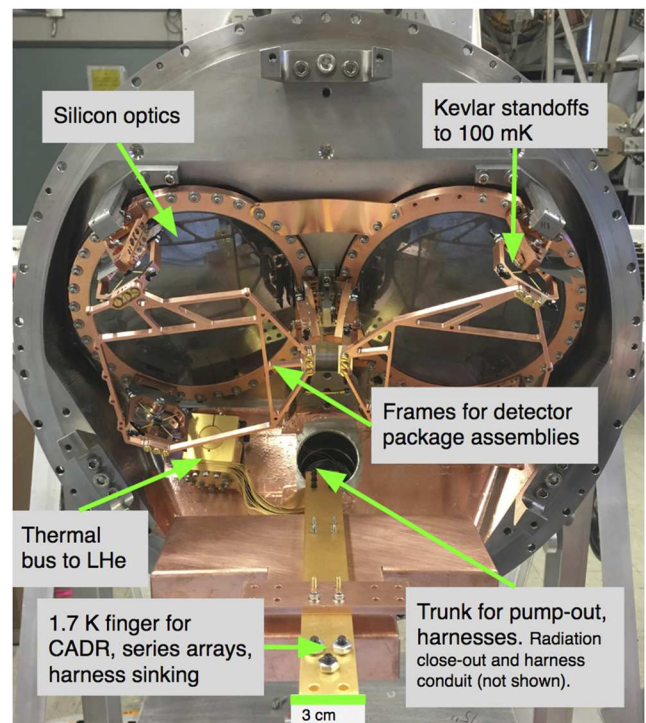


FIG. 4. Mechanical and thermal interfaces in the PIPER receiver.

orientation to the two optical paths. The receiver components are built outward from mechanical mount points on the lid.

A stainless steel thin-wall bellows trunk with 5.1 cm ID extends from the receiver to a breakout box of feedthroughs at ambient temperature and serves as a conduit for readout and housekeeping harnesses as well as receiver pump-out. All seals must operate cryogenically and remain superfluid tight. The trunk uses $3\frac{3}{8}$ in MET-SEAL flanges, and the receiver itself is 44.45 cm diameter and closed out with two indium seals.

The bellows path to the breakout box is routed to the side of the gondola to remain isolated from cryogenic gas paths on the top of the Dewar. This position allows the housing to use convenient Klein Flange (KF) seals and deploy overpressure safety, vacuum gauge instrumentation, and pump out ports that are all easily accessible on the side of the gondola and maintained at ambient temperature. High current leads for the CADR require more complex thermal accommodation (Sec. IV B). The vacuum manifold is well-protected and survived a landing where the gondola rolled over the top of the Dewar.

A multichannel electronics²⁴ (MCEs) module reads each of the time-domain SQUID-multiplexed TES arrays. The MCEs are mounted symmetrically about the midplane of the twin telescopes. Each requires 500 conductors to pass from ambient temperature to the cryogenic receiver to operate each of the two 32×40 TES arrays. The harnesses are manganin ($127\text{ }\mu\text{m}$ diameter) twisted pair to limit thermal conduction and are 4.5 m in length. The readout electronics plug directly into five Micro D-Subminiature (MDM) 100 feedthrough bulkhead plates [MPF-PI (<https://mpfpi.com/>) custom part] on vacuum housings. The readout housings connect to the central breakout housing through stainless steel vacuum bellows, which also act as Faraday cages that protect the readout from radio frequency interference.

C. Heat flow in the receiver

The receiver shell is all stainless steel to achieve CTE match with the telescope frame and Dewar, but this provides poor heat conduction from the receiver interior to the large helium bath. To overcome this limited conduction, a commercial high current vacuum feedthrough²⁵ on a CF40 flange acts as a thermal bus to carry heat from the interior to the helium bath. It is $\frac{3}{4}$ in diameter high purity copper and is superfluid tight. All thermal buses in the receiver are gold-plated²⁶ to decrease contact resistance and maintain clean joints. Redundant superfluid pumps fill a small reservoir clamped to the bus, fixing its temperature at 1.7 K. Throughout, the reference bath temperature will be 1.7 K, at equilibrium with the air pressure at 30 km minimum float altitude. The altitude is expected to remain <37 km for this balloon class and mass, giving a minimum bath temperature of ≈ 1.5 K.

A thermal bus of oxygenfree high conductivity (OFHC) copper connects to the feedthrough with a stack of gold-plated, diffusion-bonded and annealed OFHC copper foils. The foil bus accommodates the plane change from the feedthrough to the axis of the receiver, and also provides compliance for differential CTE between the copper bus and stainless steel frame, avoiding pressure on the ceramic seal of the feedthrough. The bus provides cooling for the lenses, series arrays (Fig. 5), and the CADR baseplate. The readout harnesses pass from ambient temperature to 1.7 K with no

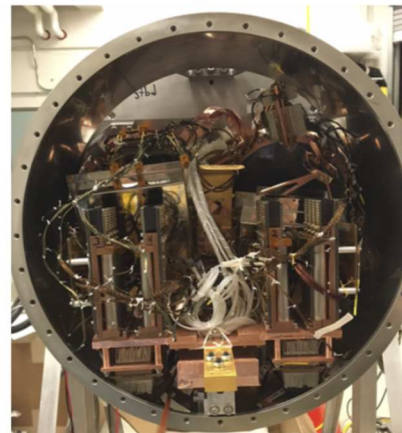
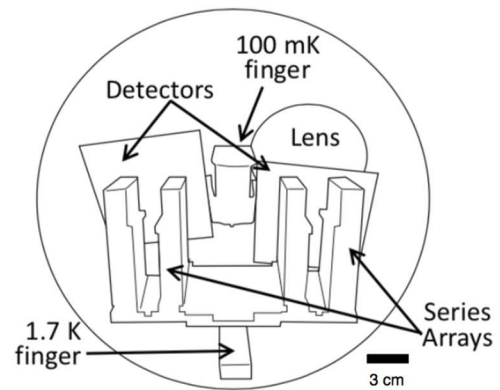


FIG. 5. Overview of the PIPER receiver before integration of the CADR.

intermediate intercepts. The helium bath of the large Dewar provides significant cooling power.

D. Testing and telescope integration

It is impractical to simulate float altitude conditions in the flight Dewar, as this requires both a high throughput pump and a gondola lid designed to support atmospheric pressure across the 1.5 m diameter of the Dewar. We instead integrate the receiver and test it in a smaller system, with open bore inner diameter 48.58 cm and depth 1.52 m. The receiver is suspended by wire rope from the lid of this test Dewar. The receiver windows can be blanked off, or covered with a beam-filling cold load. We use a cold load cooled with superfluid pumps, which remains isothermal to the pumped LHe bath. The test Dewar uses a Rietschle-Thomas vacuum pump to simulate the conditions at float altitude by pumping on the helium bath with a high throughput of $\approx 1000\text{ m}^3/\text{h}$ at ~ 1 kPa. A throttle valve can be used to reduce the throughput to simulate higher bath temperatures at a lower altitude.

The receiver, trunk carrying readout harnesses, and all vacuum interfaces remain *in situ* between testing and installation of the receiver in the telescope. Three points on the receiver lid connect to a six-point turnbuckle mount in the telescope, which allows placement relative to the optics. We use a ROMER coordinate

measurement arm to align the optics and receiver form a reference axis installed in the centerline of the telescope.

III. 100 mK COOLING

A. CADR

Figure 6 shows the CADR after installation in the receiver and Fig. 7 shows a cartoon of thermodynamic cycles²⁷ of the four stages. Table I describes the physical parameters of each stage. Here, GGG is gadolinium gallium garnet and CPA is chromium potassium alum.²⁸ Throughout S1, S2, S3, and S4 refer to ADR stages 1, 2, 3, and 4, respectively, and BP refers to the bath baseplate of the CADR. Critical parameters in Table I are the temperature where the stage rejects heat to the upper stage or bath (T_{recycle}) and the temperature from which the stage absorbs heat from the lower or detector stage (T_{cold}).

Heritage of the system closely follows a CADR prototype developed for the Constellation-X mission concept²⁹ and the architecture of the Hitomi ADR.^{1,30} The ADR crystal is suspended in the bore of a NbTi coil by Kevlar crosses at each end of the mandrel. The total envelope of the CADR is $23 \times 8.6 \times 24$ cm, and its mass is 11.3 kg.

The speed of the complete cycle that rejects heat from S1 to S2, S2 to S3, and S3 to S4 determines the CADR cooling power. The

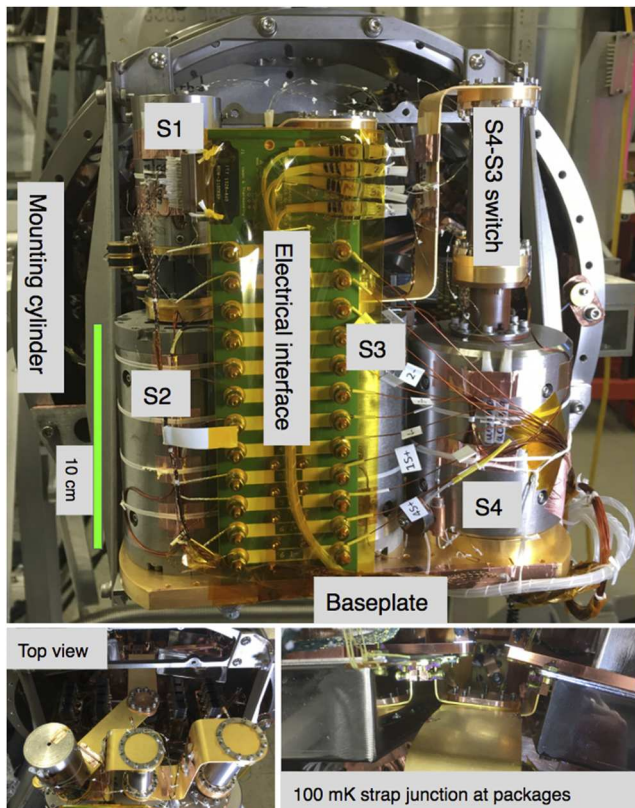


FIG. 6. Overview of the PIPER CADR, which is installed as the final element of receiver integration. The lower left inset illustrates the 100 mK bus from the CADR to the detector packages. The lower right inset shows how this bus splits to the two detector packages.

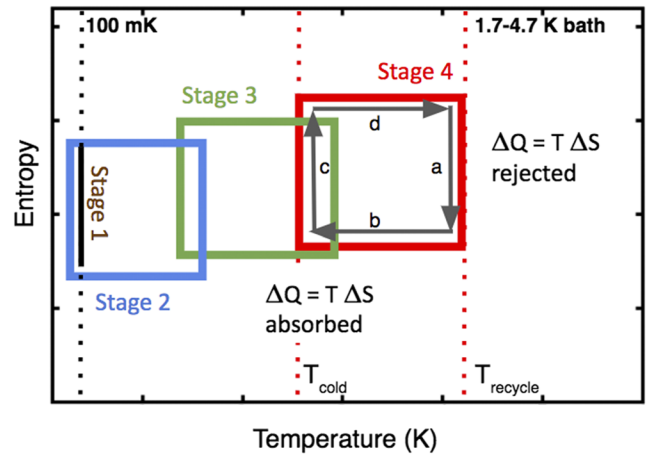


FIG. 7. Concept of multistage ADR operation. Each stage performs a thermodynamic cycle that rejects $T_{\text{recycle}}\Delta S$ energy in isothermal magnetization against an exchange (leg a). It then decouples from the exchange and adiabatically demagnetizes to an operating point (leg b) below the exchange temperature of the next stage. In leg c, the stage absorbs $T_{\text{cold}}\Delta S$. After the stage is out of cooling power (or the lower stage's recharge is complete), it decouples and adiabatically magnetizes up to T_{recycle} (leg d). In the continuous ADR, the coldest stage (Stage 1) simply serves at the operating point. When it is coupled to a colder Stage 2 through an active superconducting heat switch, heat flows from Stage 1 to 2. To remain isothermal, Stage 1 magnetizes, gaining the cooling capacity in each cycle. Axes are not to scale. Section V and Fig. 11 quantitatively describe the CADR operation.

CADR design benefits from several choices that differentiate it from a single-shot ADR. First, heat switches^{31,32} must operate quickly and can tolerate higher off-state parasitic conduction to achieve higher on-state conduction for faster stage cycling. Passive switches offer not only simplicity and reliability but also avoid a potentially radiant getter (Sec. VI D) and provide a high switching speed. Second, the paramagnetic crystal must have high thermal conduction to the thermal junctions of the pill so that the CADR may use more of the pill volume for thermal conductor interleaved into the salt.²⁸ Third, faster field excursions develop higher thermal gradients between stages (reducing the cooling capacity of the pill) and operate the NbTi coils closer to regimes where they can quench. To optimize the total cooling power, each of these three design and control parameters must trade between efficiency loss per cycle and higher recycling rate. Reference 33 describes additional mechanical and operational optimization.

TABLE I. Properties of CADR stages 1, 2, 3, and 4 (labeled S1, S2, S3, and S4). The baseplate temperature, T_{bath} , varies from 4.6 K on the ground to 1.5–1.7 K at the float altitude. S1 provides continuous cooling at T_{S1} , and S2 recharges S1 by providing cooling ≈ 10 mK below T_{S1} .

Stage	Mass (g)	Inductance (H)	T_{recycle} (K)	T_{cold}
S4, GGG	85	60.5	$T_{\text{bath}} + 0.4$ K	1.1 K
S3, CPA	100	13.6	1.35 K	0.32 K
S2, CPA	100	9.71	0.40 K	$T_{\text{S1}} - 10$ mK
S1, CPA	40	3.85	...	70–130 mK

B. 100 mK stage

The detector package and lens form an integral assembly in the receiver, directly behind the window (Fig. 4). The lens connects to the 1.7 K bath, and the detector assembly is held up by three Kevlar suspensions (Fig. 8). There is a trade-off between designing thermal stage isolation which will survive shock loading during the balloon flight, while also achieving low parasitic conduction. Following the engineering flight, the CADR had one broken Kevlar suspension and a cracked Vespel isolation shell. Both are simple to replace. A catch screw passes through the suspension to prevent the detector package from being shocked free.

The readout for each 32×40 array requires 128 conductors for the columns, 80 for the rows, and an additional 40 for 20 TES bias groups addressed in the row direction. Each conductor is $76 \mu\text{m}$ diameter superconducting NbTi with CuNi cladding. To reduce parasitic conduction and route the harness, the total harness length from the 100 mK stage to the 1.7 K lens interface is 91 cm and meanders across a Kevlar suspension. The harness is directly soldered to the printed circuit board (PCB) that is wire-bonded to the detector array. The pigtail avoids added heat capacity and volume requirements of 16 MDM connector bodies that would be required otherwise. Section VI A describes loading of the 100 mK stage through the suspension and harness.

A silicon carrier wafer holds the detector array. The carrier is mounted in the detector package by a three-point titanium flexure that maintains the array position and takes up CTE relative to the copper package. The arrays are heat-sunk to the package through gold bonds to a diamond-turned, gold-plated boss in the package. This design³⁴ follows heritage from HAWC+.³⁵

The detector package is connected to the CADR through a copper bus [99.999% purity, electronic speckle pattern interferometry (<https://www.espi.com/index.php>) metals] with four mechanical junctions. The rectangular strap sections are shaped on 3D printed forms that correspond to the interfaces in the receiver. The straps are then annealed and gold-plated. Each package has a short strap section that joins the main 100 mK bus.

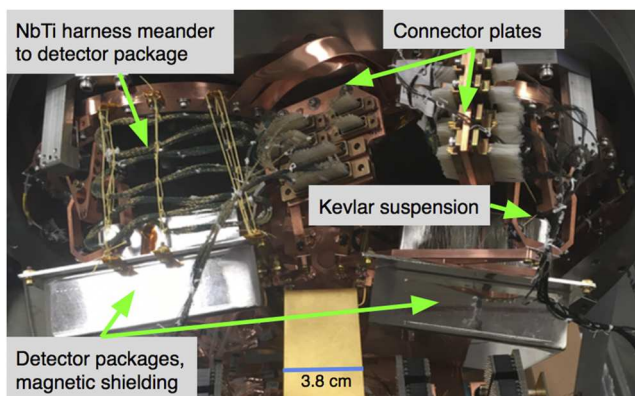


FIG. 8. Details of the 100 mK suspension. A meander in the cable harnesses to the packages decreases thermal conduction to the 100 mK detector packages from 1.7 K.

C. Magnetic shielding and materials

Each ADR stage assembly is enclosed in a 4% Fe-Si magnetic shield. When unsaturated, these maintain stray magnetic fields for each ADR stage to a level less than the Earth's field measured at the location of the detector packages.

Due to the proximity of the detector package to the receiver window, it is not feasible to place the focal planes away from the opening of a high-aspect-ratio tube. We pursue an intermediate design where the detector package is capped from behind with an Nb box and enclosed in an Amunual A4K shroud (Fig. 8). The high purity bus feeds through a slot in the shielding.

The shields add approximately 300 g of cryoperm thermal mass to the 100 mK stage. The heat capacity of the 100 mK stage is at the limit of what single-shot demagnetization can cool from 2.2 K (worst case bath temperature) to 100 mK to initialize the continuous ADR operation (Sec. V B). Magnetic materials near the hybridized 2D SQUID MUX⁸ can interfere with their operation. We use brass and titanium fasteners in the copper package (avoiding stainless steel), and do not use nickel flash for gold plating. A high aspect-ratio Nb tube encloses the SQUID series array amplifiers.

A capped cylinder of 508 μm shielding (Amunual amumetal) encloses the large flight Dewar at ambient temperature. This shield is simulated³⁶ to suppress the Earth's field by $40\times$ and $400\times$ in the axial and transverse directions of the Dewar, respectively. The large flight Dewar and smaller detector package shields were not installed in the 2017 engineering flight and will be assessed in the 2019 science flight. Repeated cooldowns, shipping, and flight shocks have the potential to work-harden the shields, requiring either replacement or reannealing depending on integrity. A Hall probe monitors shield performance. Section VI D describes the impact of saturation of the S3 shield on the detector time stream in integrated receiver tests, which included the Nb and cryoperm detector package shielding.

IV. CONTINUOUS ADR ELECTRONICS AND HARNESS

A. Control and drive electronics

Two 30U 19-in.³⁷ rack cases hold the flight electronics and batteries. The cases protect the electronics on landing, and the front panels act as side-facing radiators. All electronics that require high power dissipation are bolted to the front panel radiators and use thermal joint compound. An Earth-IR shield reflects the radiator's view to the ground to space.

The CADR coil control electronics are split functionally into a controller and high-current drive circuit. The controller resides in a 3U subrack of backplane housekeeping cards, which communicate over fiber-optic lines to the flight computer. A dedicated 3U subrack houses the high-current drive circuits (Fig. 9) and has a 0.8 cm thick aluminum baseplate for heat sinking.

The controller output actuates the voltage at the output of the high-current source and accepts commands to maintain several possible constants: (1) voltage on the coil's voltage taps, yielding a constant current rate (dI/dt), (2) resistance of the ruthenium oxide thermometry on a stage for isothermal control, and (3) a constant coil current. Note that a constant voltage from the drive circuit does not correspond to a constant voltage across the coil due to line resistance as an LR circuit, so some active control is required. A microcontroller runs the proportional integral derivative

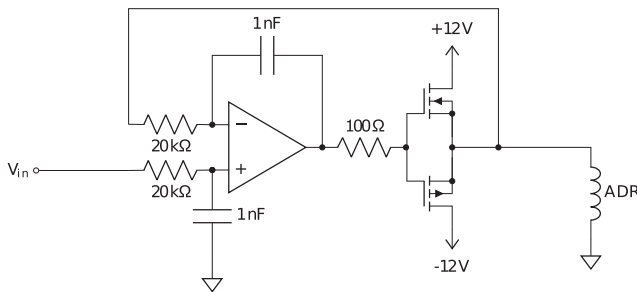


FIG. 9. The high current drive circuit for the CADR coils. The current drivers use ON Semiconductor FDP047N10 N-channel power MOSFETs for the positive swing and Vishay IRF9640 P-channel power MOSFETs for the negative swing. An op-amp configured as a differential voltage integrator drives the MOSFET network's output voltage to the value set by the controller. Several supporting elements are not shown for simplicity. An input amplifier controls the bandwidth and can scale the commanded voltages from the controller if the range is insufficient. The high-current drive circuit also provides coil current monitoring using a 0.01Ω series resistor, and coil voltage sense using a differential amplifier.

(PID) control loop at ≈ 8 Hz and 16-bit output resolution. Software services on the flight computer manage the high-level operation of the cycle (Sec. V A) and the firmware on the PID controller cards servos in response to flight computer commands. The PIPER electronics have heritage from several previous missions in the NASA Goddard balloon group.³⁸

All CADR thermometers are the Lakeshore RX-102A-CD, which we purchase uncalibrated, and calibrate in a dilution refrigerator against a Lakeshore reference. An Analog Devices AD7685 16-bit analog-to-digital converter reads the ruthenium oxide thermometry in a bridge configuration with heritage from ARCADE.³⁹ For simplicity and interchangeability, the controller retains no knowledge of the calibration from resistance to temperature. The flight software (Sec. V A) converts temperature setpoints to resistance setpoint commands using calibration curves particular to each thermometer. The controller is effectively linear in temperature for small fluctuations around the setpoint, but the PID parameters must be chosen to give stable behavior over the operational resistance range.

Batteries must power the CADR with sufficient capacity for operation throughout the mission, estimated to be at most 24 h. While lithium sulfur dioxide (SAFT G62 series) batteries power the rest of the flight electronics, we use rechargeable, sealed lead-acid batteries for the CADR due to concern for catastrophic failure of the lithium batteries in high-current quench events. Two Power-Sonic PS-121000B cells in parallel (200 Ah total) supply +12 V. Current from the -12 V supply is infrequently needed, and a smaller PS-12350B battery (35 Ah) is sufficient. For initial cooldown, the drivers must magnetize all stages and provide 8.5 A. In continuous operation, current requirements are typically 4.5 A, and peak at 6.5 A.

To be able to supply the maximum of 4 A per coil with a FET driver range of ± 8 V, the resistance of the harness to the coils must be $< 2 \Omega$. In PIPER, the harness is 4.5 m at ambient temperature, and 3 m cryogenic, necessitating the use of 18 American Wire Gauge (AWG) copper wire for the harness to the CADR. Finally, the grounding design must avoid returning large coil currents through

the common ground reference point for the sensitive PID control electronics.

B. Cooling the high-current leads

High current copper leads run from ambient temperature to the 1.7 K bath of the receiver with no other available temperature intercepts, and so must be normal metal rather than employ, for example, high-temperature superconducting breaks. Copper with excellent electrical conductivity necessarily has excellent thermal conductivity from room temperature. A run of the high-current leads through the vacuum trunk to the receiver produces thermal loading in the receiver that overwhelms the thermal bus (Sec. II C) to the helium bath.

Helium vapor and liquid in the main Dewar space precool the high current CADR leads, which then enter the receiver through an MPF-PI 8-pin (A0757-1-CF) superfluid-tight feedthrough (Fig. 10). We solder the high current leads directly onto both the inside and outside pins of the feedthrough, effectively eliminating contact resistance and producing a mechanically robust connection. The high current lines connect to the feedthrough assembly in a cryogenically compatible circular connector bulkhead to avoid a captive cable. While the feedthrough and receiver are submerged in LHe before the flight, at float altitude, the feedthrough is approximately

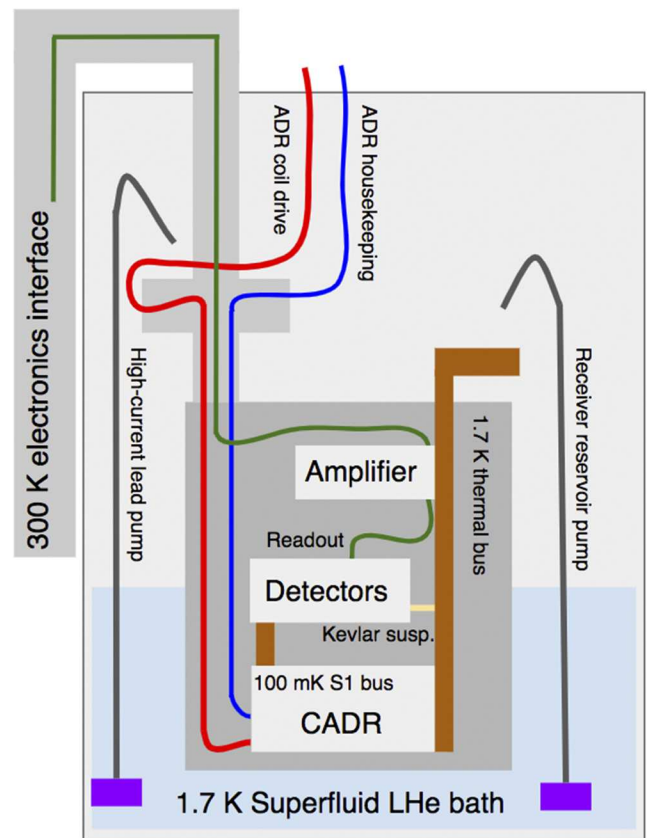


FIG. 10. Cartoon of the PIPER receiver emphasizing connections of the harnesses and thermal paths.

>60 cm above the helium bath. The connector and feedthrough itself are cooled with redundant superfluid LHe pumps, mitigating any Joule power. Inside the receiver, 0.31 mm diameter NbTi wire⁴⁰ with 0.5 mm copper cladding transmits the high current to the CADR. The copper cladding provides high thermal and electrical conduction in case of a quench or normal metal conduction, making it more robust than the 76 μm diameter NbTi (CuNi cladding) used elsewhere. Joule power in the high-current harness produces a 30 mK increase in the baseplate temperature during CADR operation. In comparison, heat rejection during the S4 cycle increases the baseplate temperature by ≈ 45 mK.

The thermometry and voltage tap channels also follow the high current lines through the helium Dewar and enter the receiver through a superfluid-tight CeramTec 41-pin (24016-01-KF) circular connector feedthrough. Both feedthroughs are mounted in a cross in the trunk, directly above the receiver (Fig. 10).

C. Electrical interfaces

A custom printed circuit board (PCB) provides a robust and repeatable electrical interface to the CADR (Fig. 6). An MDM31 connector carries the voltage taps and stage thermometry. Thermometry instrumented on the CADR stages is read over 76 μm CuNi-clad NbTi to limit parasitic conduction. All junctions of the high current leads are potential sites of single-point failure. The system must be robust to multiple integration cycles, vibration during shipment, and (as much as possible) to parachute and landing shock. Each ADR stage has a primary and redundant thermometer.

High current junctions use gold-plated copper bobbins (Lakeshore HSB-8) to which copper-clad NbTi magnet wires are wrapped and soldered. The trace size on the interface board is maximized to sink heat from the high current channels to the board groundplane and to minimize Joule heating. The bobbins are attached to the PCB with #4 fasteners and three Belleville washers. To prepare the flight instrument, we additionally stake the fasteners to maintain the junction under vibration in shipping.

V. CADR OPERATION

During the 2017 engineering flight, the S4 coil current harness opened intermittently, consistent with an air-side connector of the high-current CADR feedthrough that was not cryogenically compatible (Sec. IV B). This open-circuit appeared after shipping, and was not serviceable before the flight but was subsequently remedied by soldering directly onto the feedthrough (Sec. IV B). During the flight, S3, S2, S1, and the superconducting heat switch (SCHS) performed nominally in single-shot operation. CADR data used throughout are from integrated receiver testing at NASA Goddard. These tests use the flight electronics, software, and commanding identical to that available in the ground station. We throttle the pumped bath (Sec. II D) to simulate a range of CADR base temperatures equivalent to those expected in the balloon flight.

A. Stage controller logic and architecture

Figure 11 shows the ADR coil currents and temperatures for continuous operation. Each stage controller has the same underlying logic but has different parameters for the operating points

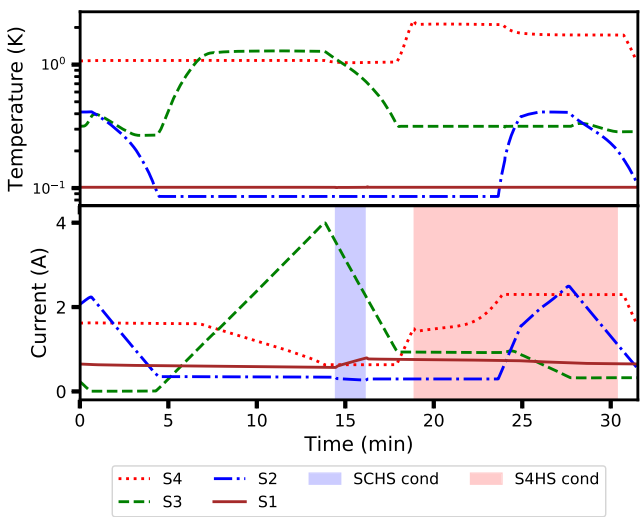


FIG. 11. One typical cycle of continuous operation of the ADR, illustrated with data from integrated receiver testing at NASA Goddard. S1 remains at 100 mK, with heat cycled out through stages 2, 3, and 4, with S4 ultimately rejecting heat to the pumped helium bath.

(Table I). Separate, asynchronous software processes on the flight computer control the ADR stages and communicate through a redis database.⁴¹

Heat switches³¹ control the heat flow between ADR stages throughout the cycle. Table II shows the properties of the heat switches connecting the four stages and baseplate. Between S4 and the baseplate, an active gas-gap switch⁴² allows for flexibility in the temperature at which S4 couples to the bath. This flexibility is essential to manage variations in the bath temperature with altitude. In contrast, the S2-S3 and S3-S4 heat switches are passive gas gap switches³² that make a transition from conducting to nonconducting in a narrow band of temperatures. Below their critical temperature, gas adsorbs on the cold side of the switch, effectively shutting down gas conduction. At higher temperatures, the gas desorbs and can effectively conduct. This yields rapid switching with no electrical input. Faster switching improves the CADR cooling power. Passive switches require a heat exchange at a fixed temperature, but this is well-matched to thermodynamics of the S2 and S3 cycles. From test data, we find off-state parasitic conduction in the S3-S2 and S4-S3 passive switches to be 3 $\mu\text{W/K}$ and 7 $\mu\text{W/K}$, respectively. All gas-gap switched employed here use ³He.

TABLE II. Properties of the heat switches between each of the CADR stages. The S2-S1 connection is a superconducting heat-switch (SCHS) that employs a lead conductor.

Connection	Condition	Type
BP-S4	$T_{\text{getter}} < 5 \text{ K}$	Active gas-gap
S4-S3	$T_{\text{S3}} < 1000 \text{ mK}$, off by $T_{\text{S3}} < 750 \text{ mK}$	Passive gas-gap
S3-S2	$T_{\text{S2}} < 300 \text{ mK}$, off by $T_{\text{S2}} < 130 \text{ mK}$	Passive gas-gap
S2-S1	$I_{\text{SCHS}} < 200 \text{ mA}$, $T_{\text{S1}} < 250 \text{ mK}$	Active SCHS

An active superconducting heat switch (SCHS) connects S2 and S1. Here, a section of superconducting lead is a poor thermal conductor in the off state. An applied field with small Helmholtz coil drives the lead normal and into thermal conduction. This allows for rapid control of the exchange between S2 and S1 across a range of temperatures (up to ~ 250 mK), providing flexibility in the continuous stage temperature and timing of the exchange. From test data, we find off-state parasitic conduction of $36 \mu\text{W/K}$.

Starting with the recycling operation, the controller first checks to see that the intercept stage is ready, providing a temperature slightly below T_{recycle} . Here, “intercept” refers to either the CADR baseplate (for S4) or the ADR stage that provides cooling. For example, S4 is the intercept for S3, and referring to Table I, S3 will wait until S4 is at 1.1 K before starting to recycle. At this point, the S3 controller seeks to maintain the S3 temperature at $T_{\text{recycle}}(\text{S3}) = 1.35$ K, $\Delta T = 0.25$ K above the S4 intercept. Heat flow to the intercept stage across ΔT requires magnetization of S3 and demagnetization of S4. The magnetization will continue until either the maximum current (hence magnetization) is reached, or the intercept is out of cooling capacity.

At this point, the stage has reached its target magnetization, and the controller can start adiabatic demagnetization to the operating temperature, T_{cold} . If the stage conducts to its intercept through an active switch, the controller will command the switch to become nonconducting. In the case of a passive gas gap switch, the stage will initially conduct to its intercept until dropping low enough that the gas gap becomes nonconducting. Once cold, the controller sets a flag that allows the next stage controller to see that its intercept is ready.

While the cyclic operation of S4, S3, and S2 is all autonomously controlled, we leave the S2-S1 interchange under manual control. Manual control comprises (1) setting the S1 PID controller at the desired detector operating temperature and (2) commanding the SCHS. SCHS control allows the operator to recharge S1 as needed and avoid interruptions during critical operations, such as detector biasing.

The stages must operate robustly through their thermodynamic cycles with limited input from the ground. In conventional balloon flights, the uplink is limited to commands with one-byte address and one-byte value, and the downlink telemetry is 57.6 kbaud. Commands from the ground perform the initial demagnetization (Sec. V B) to the continuous state and then initiate the S4, S3, and S2 controller state machines, which operate autonomously.

B. Initializing the continuous state

The CADR stages and detector packages start at the CADR baseplate temperature (1.7–4.7 K), and so are far from their desired operational temperature ranges and have high heat capacity. Figure 12 shows the progress of the initial demagnetization. To begin the cycle, we magnetize S4 to 2.8 A, S3 to 4 A, S2 to 2.5 A, and S1 to 800 mA with the 4-BP switch coupled to the bath. Once the S4 is nearly in equilibrium with the bath temperature, we decouple the 4-BP switch and demagnetize S4 to a temperature of 1.1 K. This cooling pulls S3, S2, S1, and the detectors to 1.1 K. S4 cannot pull the upper stages much below this temperature because the passive 3–4 switch conduction drops rapidly (Table II). We then demagnetize S3 to 0 A, reaching 550 mK from a 2 K base plate. From here, we demagnetize S2 and reach 100 mK. After S2 has pulled S3 below

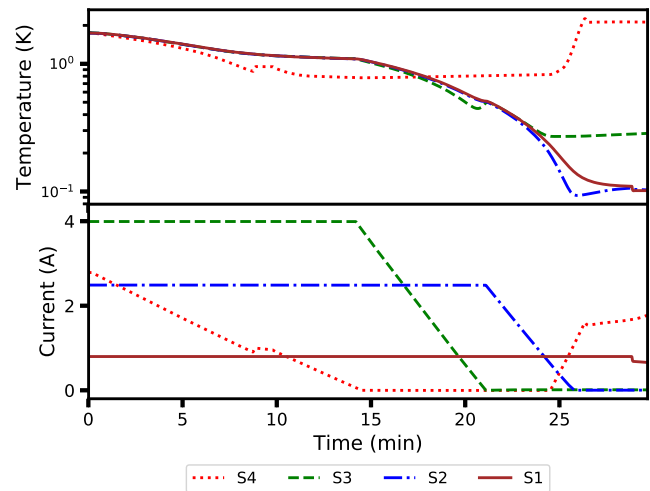


FIG. 12. Preparing continuous operation by sequentially demagnetizing the stages. Once S1 reaches 100 mK, S4 remagnetizes. It proceeds to recharge S3, then S3 recharges S2, and S2 recharges S1. Data are from integrated receiver testing at NASA Goddard.

300 mK, we start the S4 cycle state machine. Once S2 is demagnetized, S1 can start continuous operation.

C. Ramp rates

The field ramp rate must be optimized to account for a range of conditions, principally to minimize thermal gradients while also achieving a high cycle rate. Each stage in the CADR has three typical cooling profiles that each have optimal ramp rates: (1) cooling downstream stages during initialization (Sec. V B). Here, the stage is cooling the high heat capacity of subsequent stages and must ramp relatively slowly at to remain within $\approx 80\%$ of the lower stage temperature (Fig. 12), requiring 15 min, 7 min and 4 min for demagnetization of S4, S3, and S2, respectively. The heat capacity drops as the 100 mK stage cools from 1.7 K, allowing progressively faster operation, and (2) ramping between isothermal exchanges. Here, a stage decouples from other stages and can magnetize or demagnetize quickly to change temperatures, requiring approximately 5 min for each stage to demagnetize, and (3) isothermal exchanges between ADR stages during recycling operations of S4, S3, and S2 are 5–10 m. Figure 11 shows the latter two ramp periods. The system has excess cooling power (Sec. VI A), so we have chosen conservative rates which are far from a regime where the magnets can quench.

D. Variations in operational envelopes

The changes in pressure during the flight produce a range of LHe bath temperatures. During the ascent, the falling pressure pumps the LHe bath and the temperature drops from 4.3 K on the ground to as low as 1.5 K at balloon float. The superfluid pump system cannot operate until the LHe bath is below 2.17 K, achieved at ≈ 24 km altitude. After reaching float altitude, a zero-pressure balloon can sag in altitude overnight, causing the bath temperature to increase. The engineering flight had more sag than expected and resulted in a 0.1 K bath increase. At nominal float altitudes

30–37 K, the CADR baseplate runs between 1.5 and 1.7 K. Tests of the integrated receiver at NASA Goddard verified operation of the CADR up to the worst-case flight conditions of a 2.4 K baseplate.

The CADR must have robust, primarily autonomous operation across the baseplate temperature range with minimal cycle parameter changes. We use the significant spare capacity of the S4 to take up these differences in CADR baseplate temperature. S3, S2, and S1 all operate at their nominal parameters regardless of the baseplate temperature, as they must to continue to work with passive switches at fixed transition temperatures. S4 uses a GGG rather than CPA (as is used in S3, S2, and S1), and can operate from ~ 700 mK to 5.8 K. S4 provides significantly more cooling capacity than is needed at float altitude.

E. Steady-state operation

The CADR has approximately $5\times$ the necessary cooling power at float altitude conditions (Sec. VI B). In the autocycle state machine (Sec. V A), unused capacity results in stage currents that rise with each complete cycle of the CADR. S2 and S1 reach their maximum currents on each cycle, shortening the S3 cycle. Although the cycle reaches a steady-state, we choose to park the autocycle with stages at their cold temperatures when there is some spare cooling capacity (e.g., the build-up of excess current on S2 and S1). This (1) minimizes parasitic conduction through the heat switches, (2) reduces variations of the magnetic fields (reducing detector interaction, Sec. VI D), and (3) uses less battery power.

F. Quenches

Quenches can be caused by magnetizing or demagnetizing at high rates, over-magnetizing a coil, or by operating a coil at temperature >5.2 K. Default parameters and working limits in the software controller and firmware prevent all three quench conditions. The ADR coils have protection diodes, and all quenches to-date have produced thermal transients, but no coil damage. It takes nearly 2 h to recover from a quench (including stabilization, remagnetization, and demagnetization to continuous operation at 100 mK).

The S4 coil initially quenched below its required 2.8 A operating current and must be trained up to its operating range.⁴³ This behavior is believed to be due to a mechanical defect (running in microcracks in the coil potting) in the specific coil, which will be replaced in subsequent work. Training requires six quenches over 45 min in the cryogenic commissioning of the receiver. Provided the magnet remains at cryogenic temperatures, no additional quenches before launch are required. Coils for the S1, S2, S3, and SCHS do not require retraining and are only exercised up to 100 mA in the preflight check.

G. Comparison to single-shot ADR systems

The CADR provides several advantages over single-shot operation aside from a continuous cold stage temperature. A single-shot ADR stage requires the capacity to cool for the duration of science operation, here >12 h. In contrast, a continuous stage must be sufficient to hold for <20 m of a single-stage cycle (Fig. 11), yielding a reduction in salt volume and mass, which propagates to the coil and shield volumes.²⁹ For example, in cryogenfree mode from 4.5 K, the 3-stage ADR in Astro-H³⁰ has a hold time of 15 h for

<1 μ W load at 50 mK and weighs 15 kg. Analogous operation of the CADR provides >4 μ W cooling with less mass. A multistage design also provides flexibility to accommodate multiple bath temperatures (through different salt types) and to reduce parasitic power on cold stages.⁴⁴

Single-shot, single-stage ADRs must limit parasitic conduction to achieve long hold times, and so benefit from mechanical heat switches¹⁶ or backing coolers.¹² In contrast, lower interstage parasitic power in the multistage CADR allows for the use of gas-gap switches, which avoid mechanisms, or any electrical input (in the case of passive³² switches). Low parasitic loading also means that the coldest stage can be relatively small and low inductance (Table I), making it easier to control. All stages are driven by identical PID current controllers (Sec. IV A) and stage control logic (Sec. V), which mitigates the added complexity of multiple stages. Section IV A describes the power requirements, which are comparable to the mean of a single-shot stage with 9 A peak magnetization. Spreading the cooling power over many short cycles also levels loading in cryocooler systems,³³ making it of interest for ultralong duration ballooning or space applications.

VI. PERFORMANCE

A. 100 mK stage loading

We infer loading of 2.1 μ W on the 100 mK stage (Table III), calibrated by applying a known power to a heater and measuring the difference in the S1 coil current ramp rate at a fixed temperature. We estimate the NbTi readout harness loading from both arrays to be 610 nW, and the Kevlar suspensions to be 1.1 μ W, giving 1.7 μ W in total. The harness from the 100 mK detector package to the lens traverses a 53 cm meander before being anchored at the lens at 1.7 K. The three Kevlar suspensions for each package have four 16 mm links, each of 356 μ m diameter. Additionally, a Kevlar ladder structure secures the harness meander and comprises 12 Kevlar runs across 8.4 cm, in each package. Refs. 46 and 47 provide the basis for an estimate of loading in the readout harness and the Kevlar suspension, respectively. The PIPER detector readout harness is NbTi superconducting with copper-nickel cladding in a bundle of twisted pairs (A related Ref. 45 considers a phosphor-bronze clad NbTi harness which is woven into a ribbon.).

The model underestimates the measured loading, but we note that thermal conduction varies across material preparations and

TABLE III. Measured operational parameters of the CADR. A range of parasitic loading is given for when the intercept stage (e.g., S2 for S1) is at a higher temperature recharging. The difference in power represents the parasitic loading across the heat switch. The charge ratio is the ratio of current gained in the magnetizing stage to current lost in the demagnetizing stage at each isothermal exchange (S1 charges no stage, so no ratio is given).

Stage	$\Delta T/\Delta I$ (mK/mA)	Charge ratio	$P_{\text{parasitic}}$ (μ W)	I_{max} (A)
S4	1	3.3	40	2.8
S3	0.31	2.1	(3–10)	4
S2	0.15	4.3	(<1–4)	2.5
S1	0.1	...	(2–13)	0.8

manufacturers, and in literature. In Ref. 45, the phosphor bronze cladding dominates conduction. Applying their range of models to our harness results in loading 0.9–2.0 μW , across the range of literature values for conduction of phosphor bronze.⁴⁵ When S2 returns to $T_{\text{recycle}}^{\text{S2}} = 0.40\text{ K}$ to recycle against S3 over 6 min, the S1 loading increases to 13 μW , implying 36 $\mu\text{W/K}$ parasitic conduction across the SCHS.

B. Cycle thermodynamics

The applied coil current is a convenient variable for understanding the thermodynamics of the continuous cycle. When the magnetic field in the crystal exceeds the internal crystal field, (1) the rate of change in entropy, and so power exchanged at constant temperature, is proportional to the change in current, (2) for adiabatic demagnetization of a decoupled stage, the crystal dominates the heat capacity and its temperature directly scales with the magnetic field or, equivalently, coil current. Additionally, the stage controllers directly measure the coil current, and so it provides a conveniently measurable quantity that does not require a model for the entropy of the spins or the distribution of the magnetic field in the crystal. Table III gives $\Delta T/\Delta I$ in adiabatic demagnetization, the ratio of current exchanged between CADR stages, stage loading, and the maximum current for each stage.

In typical flight operation with a 1.7 K CADR baseplate and 100 mK detectors, the S1 coil ramp rate is measured to be 5.8 mA/min. Hence, over the approximately 30 min period where the upper stages recycle, I_{S1} falls by 174 mA. S1 can be magnetized by at most 800 mA, and so S1 can provide a cooling power several times higher ($\approx 10\text{ }\mu\text{W}$) than needed in PIPER's flight. Additionally, S2 receives approximately $\Delta I_{\text{S2}} = +176\text{ mA}$ for each recycle with S3, allowing $\Delta I_{\text{S1}} = +756\text{ mA}$ in exchange with S1 (Table III), yielding $\approx 10\text{ }\mu\text{W}$ cooling in steady-state. Note that higher capacity is possible²⁹ through cycle optimization, but is not needed here.

Coil currents can also be used to model the operating temperature range. Starting from S2's exchange with S3 at 400 mK and S2 maximum current of 2.5 A, S2 can drop to a temperature of 40 mK with 100 mA remaining. From there, $\Delta I_{\text{S2}} = 40\text{ mA}$ can recharge S1 for continuous operation at 50 mK and yield $I_{\text{S2}} = 60\text{ mA}$ surplus. (In this low-current regime, the self-field effects become more significant, and the cooling power per unit current drops.) In practice, several ADR cycles are required to build the current in S2 necessary to achieve this low-temperature operation, and CADR operation cannot begin until ascent. For each ADR cycle with $\Delta I_{\text{S2}} = +176\text{ mA}$, approximately 40 mA is needed to remagnetize S1. The remaining current can be used to drop the temperature of S2 and S1 by roughly 15 mK per cycle. Hence after two cycles starting at 100 mK, the arrays can operate at 70 mK, which we take as a practical lower limit for operation in a 12 h science flight.

Operation of the detector array above 130 mK requires an additional step of dropping S1 back to <130 mK before coupling to S2 because the S3-S2 passive switch will become conducting for $T_{\text{S2}} > 130\text{ mK}$. S2 can provide an intercept at 120 mK (10 mK below S1) without driving the S3-S2 switch to conduction. Continuous operation is therefore limited to <130 mK. These thermodynamic constraints combine to give an operating range 70–130 mK with considerable margin in cooling power at 100 mK in flight conditions.

C. Stability

At the three thermometers on the detector packages, we find $17\text{ }\mu\text{K}/\sqrt{\text{Hz}}$ noise. The cross-power spectrum between the thermometers measures coherent temperature fluctuations and is white with level $4.8\text{ }\mu\text{K}/\sqrt{\text{Hz}}$. This is consistent with expected noise in the S1 PID control loop, which is based on a thermometry readout which has noise at the level $5.4\text{ }\mu\text{K}/\sqrt{\text{Hz}}$.

The thermometry is read at 1 Hz, so we are unable to directly measure temperature fluctuations in the PIPER science band (4 Hz and harmonics of the VPM modulator). We find that the response of the detector package temperature to fluctuations on the PID current controller is fit with a low-pass filter with one pole at $0.8 \pm 0.2\text{ Hz}$. Hence, $4.8\text{ }\mu\text{K}/\sqrt{\text{Hz}}$ fluctuations would roll off to $< 0.16\text{ }\mu\text{K}/\sqrt{\text{Hz}}$ at frequencies $> 4\text{ Hz}$.

Through conduction in the suspensions, the 100 mK temperature is sensitive to variations in the baseplate temperature and S4, S3, and S2 pill temperatures. The ratio between variations in these stage temperatures and the 100 mK stage temperature (measured at the detector carrier board) is $\{1 \times 10^{-4}, 6 \times 10^{-7}, 6 \times 10^{-6}, 2 \times 10^{-5}\}$, respectively for the baseplate, S4, S3, and S2. The largest is the susceptibility to the baseplate temperature because every suspension connects the ADR stage to the baseplate. Temperature variations in these respective stages during CADR cycling at balloon float are $\{0.04, 1.2, 0.9, 0.3\}\text{ K}$, resulting in temperature variations of $\{4.0, 0.7, 5.4, 6.0\}\text{ }\mu\text{K}$. These deviations are below the level of readout noise, and we determine these susceptibilities using cross correlation over 20 min of data. Deviations follow known templates from the measured stage temperatures, aiding in modeling (as necessary of the TES data).

For TESs stuck in the superconducting branch after an IV curve, we apply a bias to drive them normal and back onto transition. Maximum biasing heats the detector carrier wafers by up to 70 mK, heats the packages by 0.5 mK, and produces a 0.1 mK ripple at the PID-controlled S1. The system settles within 5 s, and this represents negligible loading on S1 during regular operation.

We can convert temperature fluctuations to equivalent power fluctuations⁴⁸ on the detectors using the susceptibility $G(T_b/T_c)^{n-1}$, where G is the thermal conductivity, T_b and T_c are the bath and critical temperature of the detectors, respectively, and n is the index of the TES conduction law. This yields $\approx 4.2\text{ pW/K}$ to equivalent Joule power so that temperature noise at $< 0.16\text{ }\mu\text{K}/\sqrt{\text{Hz}}$ becomes $< 7 \times 10^{-19}\text{ W}/\sqrt{\text{Hz}}$ equivalent noise in absorbed power. Implied fluctuations are below the dark per-detector median noise-equivalent power (NEP) based on measured TES parameters for array 1 and array 2, $2.4 \times 10^{-18}\text{ W}/\sqrt{\text{Hz}}$ and $4.5 \times 10^{-18}\text{ W}/\sqrt{\text{Hz}}$, respectively, compared to expected photon noise $8 \times 10^{-18}\text{ W}/\sqrt{\text{Hz}}$ (in the 170–230 GHz band). 100 mK stage temperature fluctuations appear as a common mode across both arrays, aiding in subtraction. Future work aims to improve thermal stability through improved excitation and PID optimization.

D. Detector interaction

We observe several modes of interaction with the detectors. These appear approximately as a common mode on the detectors, though the susceptibility to radiative, thermal and magnetic

effects may vary in detail across the arrays. We will describe the amplitude of the pickup inferred from feedback DAC counts in terms of equivalent (Joule) detector power through a conversion $35 \text{ pW}/\mu\text{A}$. Most of the effects are also slow compared to the science signal, which is modulated by the 4 Hz throw of the VPM.

The S3 magnetic shield in the first flight unit was undersized slightly for the required field range and saturated for fields above 80% of the pill capacity. Due to the difference in temperature between the S4-S3 and S3-S2 passive switches, S3 must be run at its full capacity to recharge S2, leading to a stray field during cyclic operation. Pickup on the detectors produces an equivalent absorbed power $\approx 1 \text{ fW}$ that spans $\approx 3 \text{ m}$. In future versions, the shield will be thicker. Additionally, S4 has significant spare capacity, allowing the S4-S3 passive switch temperature to be lowered, reducing the necessary field excursion of S3 during its cycle. The Helmholtz coils of the SCHS are unshielded, but these reach a central field of $\approx 100 \text{ mT}$. There is no evidence of SCHS interference in the time-ordered data.

The BP-S4 switch is active and driven by a heated getter on a stem outside of the S4 assembly. We heat the getter to 11 K in normal operation to make the switch conducting, and find that this produces a 3 fW response in the detectors, implying a coupling of 3×10^{-4} into the detector throughput. This detector loading varies slowly over 12 min and is a small fraction of the saturation power (0.3 pW for Arr1 and 1 pW for Arr2). Use of a passive heat switch for the 4-BP connection could avoid getter radiation in most systems. However, the PIPER CADR requires an active switch to accommodate changes in bath temperature during the flight. The next iteration of the CADR will directly control radiation from the BP-S4 getter.

S1 alternates between rejecting (during isothermal magnetization against S2) and absorbing heat while its PID control loop seeks to maintain a constant temperature. Cooling from S2 pulls S1 down by 0.24 mK during the recharge operation and produces a 1 mK transient over 10 s. The transient effects are rapid and can be masked in $\approx 20 \text{ s}$ of the data across the 30 min CADR cycle, representing a small loss of data. In steady-state operation at the base temperature of balloon float, S1 recharge against S2 requires $\approx 2 \text{ m}$ of the $\approx 30 \text{ m}$ continuous cycle time. Future work will implement a feed-forward controller to respond to heat flow out of S1 from the known temperature difference and use the PID loop to suppress residual fluctuations.

VII. SUMMARY

We have described the PIPER receiver, which cools two kilopixel arrays in a superfluid-tight enclosure in an all-cryogenic telescope in an LHe bucket Dewar. This publication is the first report of the performance of a continuous adiabatic refrigerator to cool continuum TES arrays. Control software and electronics are refined to provide highly automatic control of the CADR stages. The CADR provides cooling between 70 and 130 mK with high stability. CADR testing and tuning focused on achieving robust operation and control in flightlike conditions during integrated receiver testing (Sec. II D), and we have outlined areas for future optimization. PIPER is expected to have its first science flight in October 2019 from Ft. Sumner, New Mexico.

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